

FIGURE 2 (Prior Art)

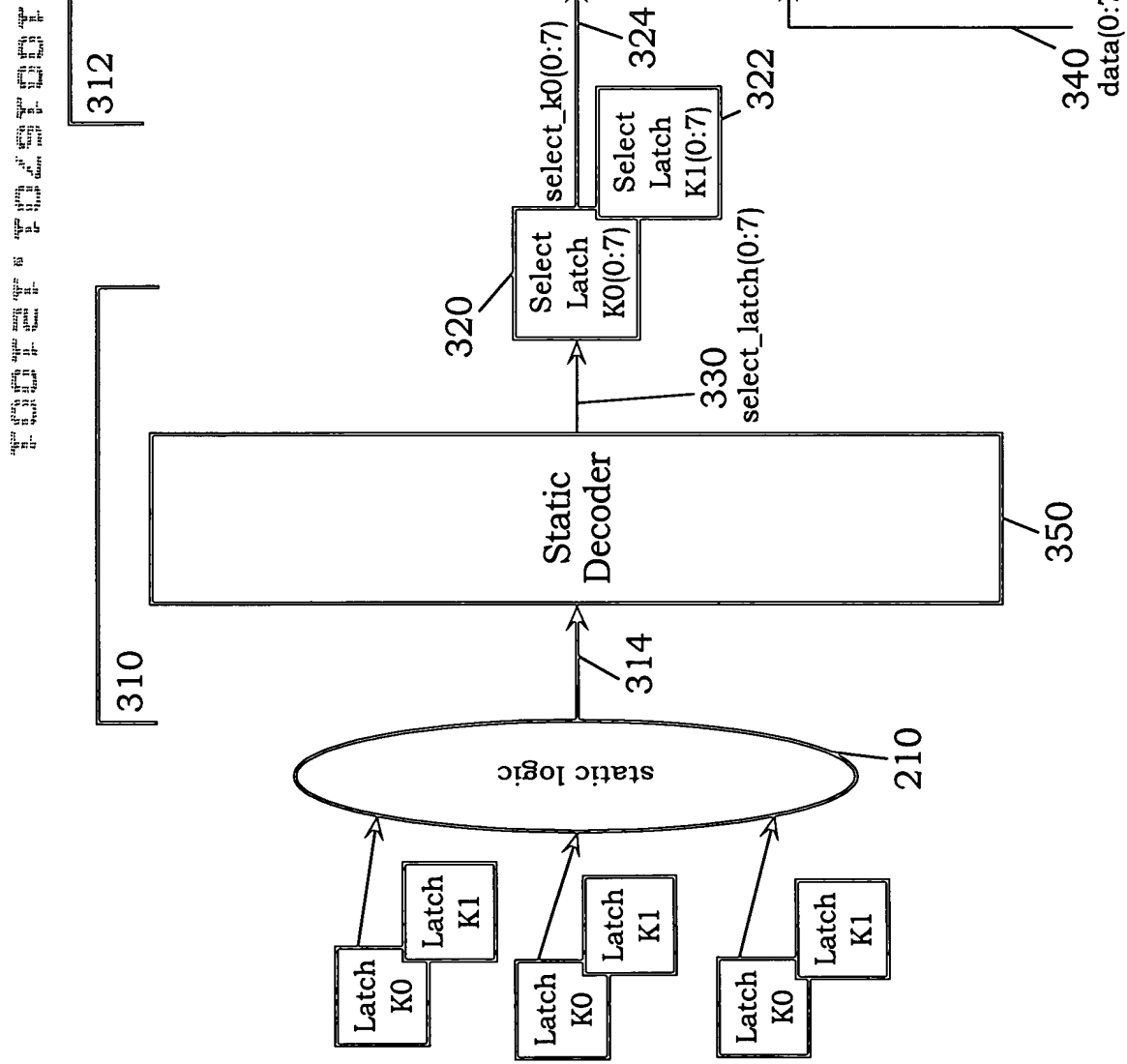


FIGURE 3 (Prior Art)

FIG. 4 is a schematic diagram of a prior art circuit 410. The circuit 410 includes a precharge node 420, a clock input CLK, and a series of data inputs data(0) through data(7) and select inputs select\_k0(0) through select\_k0(7). The circuit 410 is shown in a perspective view with a top surface and a bottom surface. The circuit 410 is a prior art circuit.

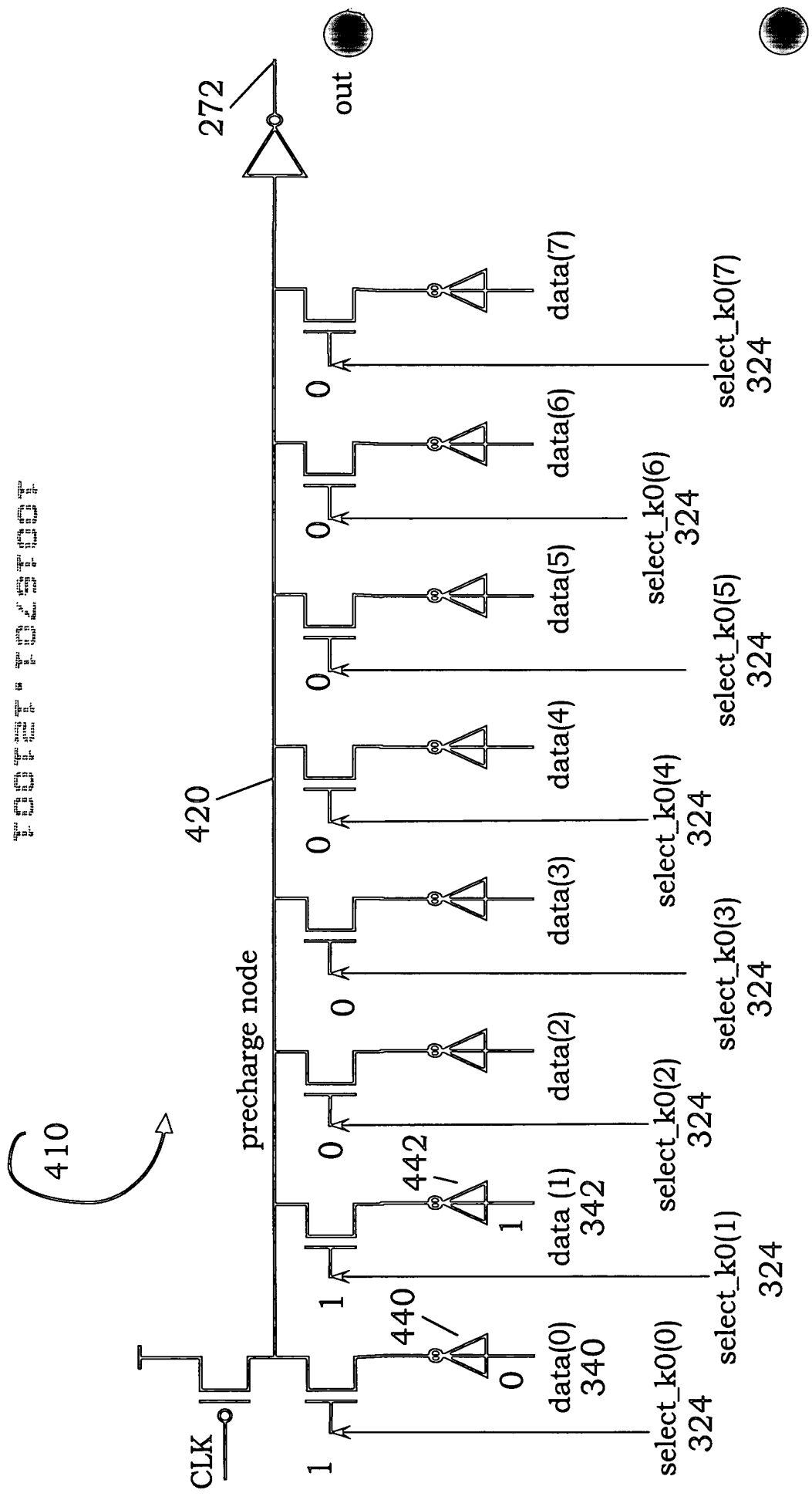


FIGURE 4 (Prior Art)

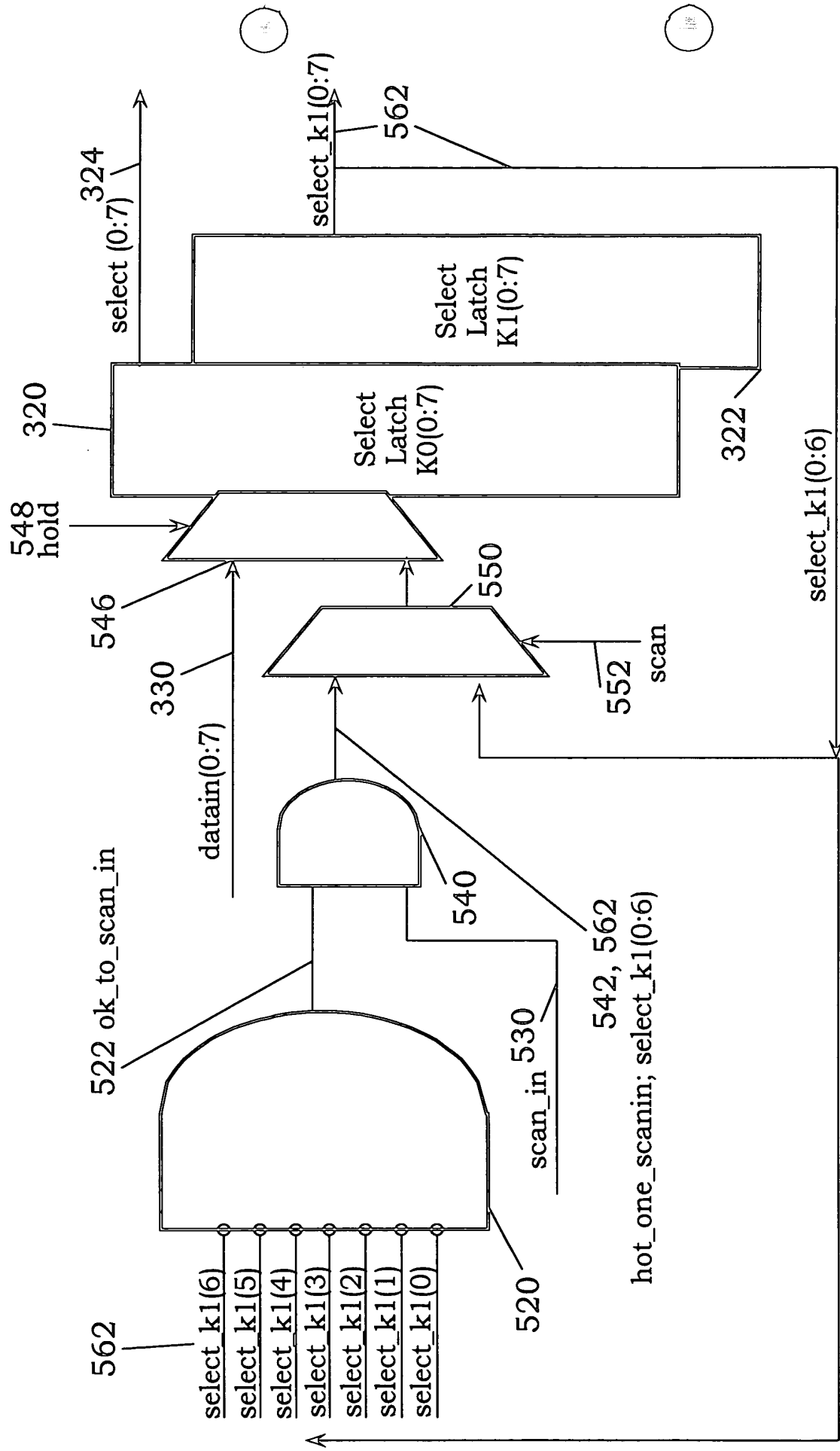


FIGURE 5

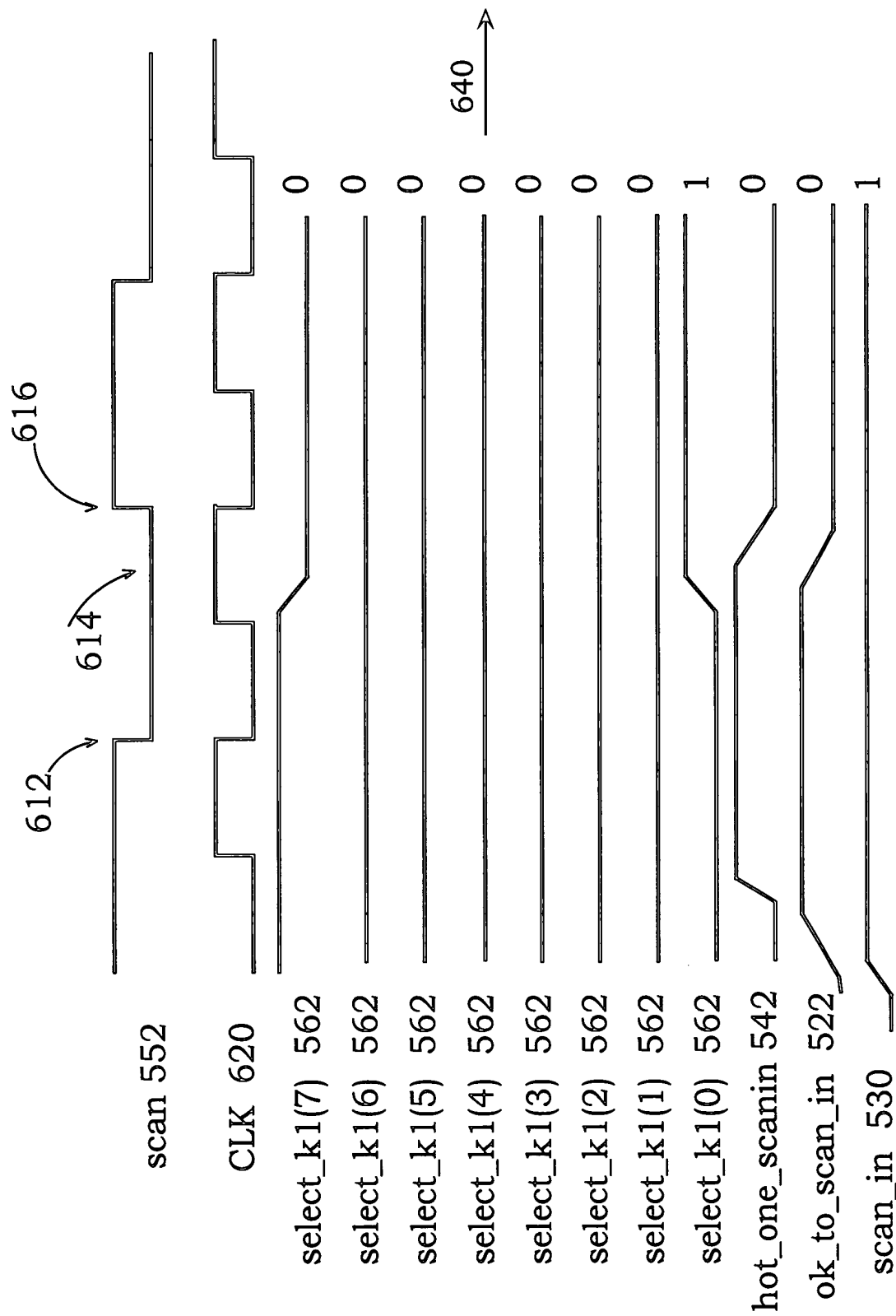


FIGURE 6

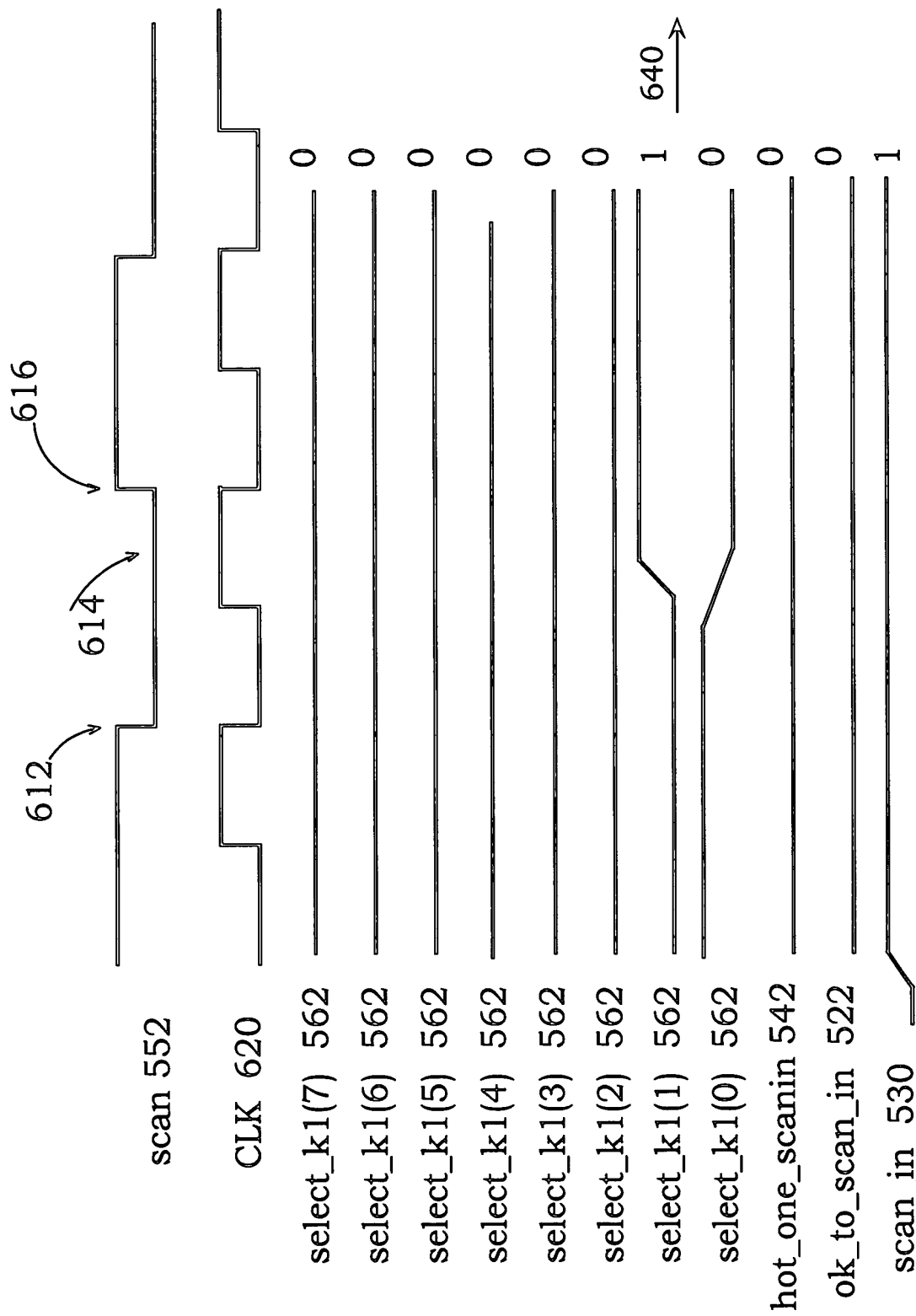


FIGURE 7

FIG. 8 is a block diagram of a system architecture for a scan chain, showing the flow of data and control signals between various components.

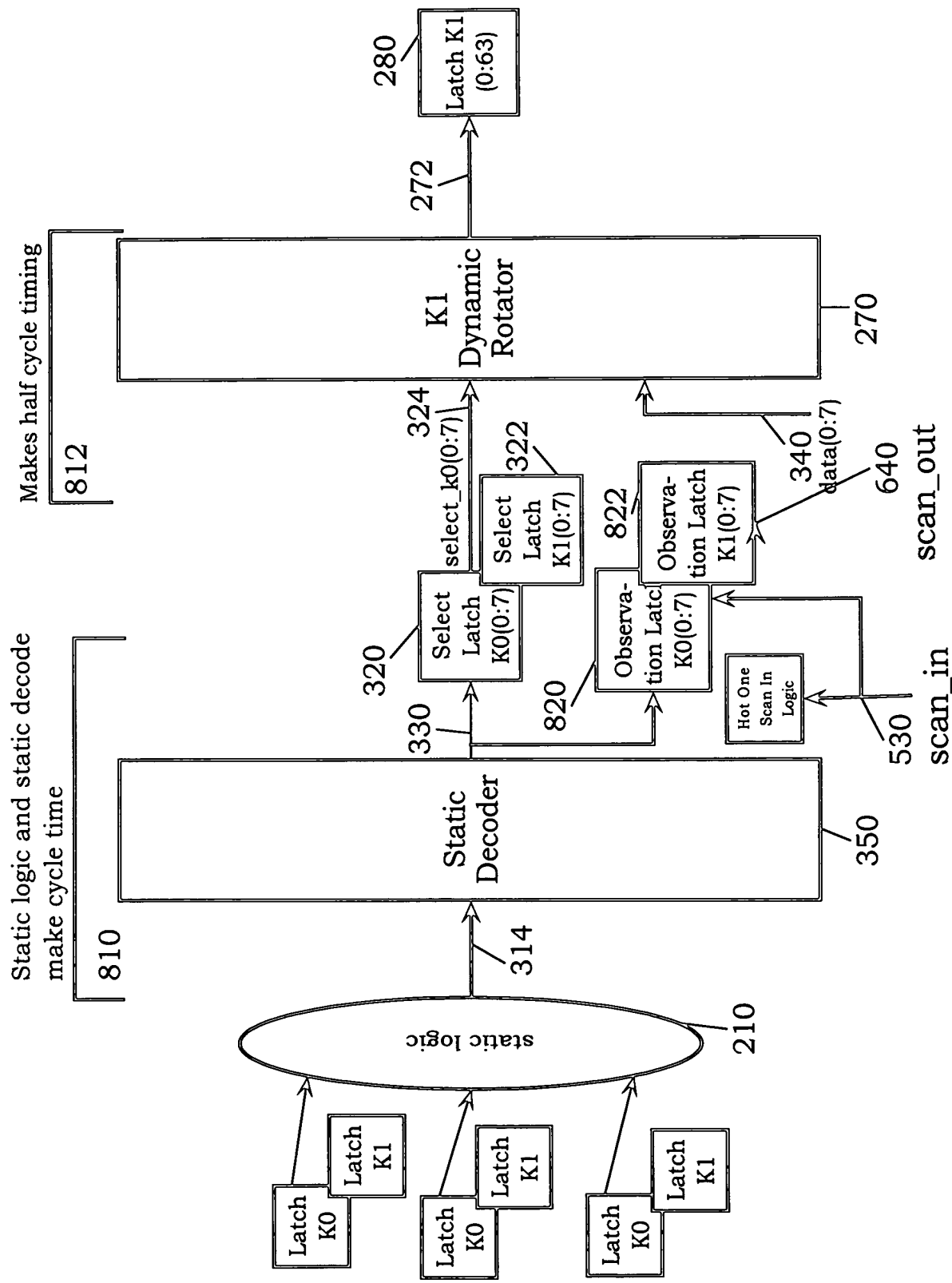


FIGURE 8